



Date: 08th December, 2022

To
General Manager - Listing
The Department of Corporate Services – CRD
BSE Ltd,
PJ Towers, Dalal Street,
Mumbai – 400 001.

Dear Sir/Madam,

Sub: Submission of a copy of press release - A World-Class VLSI & Embedded Systems Training Facility, “MosChip Institute of Silicon Systems (M-ISS)” in Collaboration with Cadence Design Systems was Inaugurated by Shri. Jayesh Ranjan in Hyderabad on 7th December, 2022.

With reference to the subject cited above, please find attached a copy of the press release on the item titled **“A World-Class VLSI & Embedded Systems Training Facility, “MosChip Institute of Silicon Systems (M-ISS)” in Collaboration with Cadence Design Systems was Inaugurated by Shri. Jayesh Ranjan in Hyderabad on 7th December, 2022”** being published in various newspapers on 08th December, 2022. The press release was issued by MosChip Institute of Silicon Systems Private Limited (M-ISS) the wholly owned subsidiary of MosChip Technologies Limited.

Kindly take the above information on your records.

Thanking you,

Yours faithfully,
For MosChip Technologies Limited,

CS Suresh Bachalakura
Company Secretary

MosChip Technologies Limited

CIN: L31909TG1999PLC032184

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TITLE: A World-Class VLSI & Embedded Systems Training Facility, “MosChip Institute of Silicon Systems (M-ISS)” in Collaboration with Cadence Design Systems was Inaugurated by Shri. Jayesh Ranjan in Hyderabad on 7th December.

(Hyderabad, 7th December): MosChip Institute of Silicon Systems (Est. 2011), A fully owned subsidiary of MosChip Technologies, (India’s first publicly traded fabless semiconductor company) has been contributing to the eco-system by training thousands of students in VLSI design since 2011. The Institute is moving into a world-class facility and signing a partnership with Cadence Design Systems to expand and enhance the program.

The Institute has been collaborating with a leading EDA tool provider, Cadence Design Systems, to train students in using cutting edge CAD tools and create talent pool for rapidly growing semiconductor industry.

The state-of-the-art infrastructure is spread across 15,000 sq. Ft and can accommodate more than 600 students per year (300 per batch at a time) to train students in multiple domains like Physical Design, Analog Layout, Design Verification & Embedded Systems.

Venkata Simhadri, MD & CEO of MosChip says, “Our training facility has been providing world-class training since 2011 and trained thousands of students who are employed in almost every leading semiconductor company in the world. Our new training facility gives us the ability to serve the growing demands of the industry and I thank our EDA partners for their support over the years”.

For more details, visit: www.m-iss.in & Follow M-ISS on social media: [LinkedIn](#), [Facebook](#), [Twitter](#), [Instagram](#), [You Tube](#).

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MosChip Institute of Silicon Systems Private Limited

(A wholly owned subsidiary of MosChip)

CIN: U72900TG2011PTC072006

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